

```
#Makefile für Praktikum  
#Prekolloquium Aufgabe 1
```

Kommentare

```
CFLAGS    := -O0  
CC        := arm-none-eabi-gcc  
TARGET    := prog  
SRC       := prog.c  
OBJECT    := prog.o  
RM        := rm rf
```

Variablen

```
$(TARGET) : $(OBJECT) $(SRC)  
            $(CC) $(CFLAGS)
```

Dependencies

```
clean:
```

Body / Build Commands

```
$(RM) $(OBJ) $(TARGET)
```

Targets