

\_AMP\_CORE0 TRACE32

Trace Probe Perf Cov ZYNQ7000 uC/DS Window Help



B:Lisk:auto

Go || Break Mode t. ↵ Fin

Display Tasks

Display Events

Display Flags

Display Timers

Display Memory Partitions

Stack Coverage

```
@ set irq & piq bits in CSR to DISABLE all interr
```